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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/631,804 08/01/2003		Naoki Kubo	Q76384	1990
23373 7.	590 12/23/2003		EXAMINER	
SUGHRUE MION, PLLC			WILLIAMS, ALEXANDER O	
2100 PENNSYLVANIA AVENUE, N.W. WASHINGTON, DC 20037			ART UNIT	PAPER NUMBER
			2826	•
	•		DATE MAILED: 12/23/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

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Part of Paper No. 20031212

	Application No.	Applicant(s)				
	10/631,804	KUBO				
Office Action Summary	Examiner	Art Unit				
	Alexander O Williams	2826				
The MAILING DATE of this communication app	pears on the cover sheet with the	correspondence address				
Period for Reply	VIO OCT TO EVOIDE AMONT	I/S) EDOM				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.7 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute - Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).  Status	136(a). In no event, however, may a reply be to ly within the statutory minimum of thirty (30) da will apply and will expire SIX (6) MONTHS from e. cause the application to become ABANDON	imely filed  ays will be considered timely.  m the mailing date of this communication.  ED (35 U.S.C. § 133).				
1) Responsive to communication(s) filed on	<u>_</u> .					
2a) ☐ This action is <b>FINAL</b> . 2b) ☑ This	This action is <b>FINAL</b> . 2b)⊠ This action is non-final.					
3) Since this application is in condition for allowated closed in accordance with the practice under	ance except for formal matters, p	rosecution as to the merits is 453 O.G. 213.				
Disposition of Claims						
4) Claim(s) 1-17 is/are pending in the application	☑ Claim(s) <u>1-17</u> is/are pending in the application.					
4a) Of the above claim(s) is/are withdra	awn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-17</u> is/are rejected.	•					
7) Claim(s) is/are objected to.	- u ata ati a a sa ancius anno at					
8) Claim(s) are subject to restriction and/	or election requirement.	,				
Application Papers		)				
9) The specification is objected to by the Examin		•				
10) The drawing(s) filed on is/are: a) □ acc						
Applicant may not request that any objection to the						
Replacement drawing sheet(s) including the correct						
11) The oath or declaration is objected to by the E	Examiner. Note the attached Onit	Se Addon of format 10 102.				
Priority under 35 U.S.C. §§ 119 and 120		(a) (d) as (f)				
a) ☐ All b) ☐ Some * c) ☐ None of:  1. ☐ Certified copies of the priority document 2. ☐ Certified copies of the priority document 3. ☐ Copies of the certified copies of the priority application from the International Bures * See the attached detailed Office action for a list 13) ☐ Acknowledgment is made of a claim for domest since a specific reference was included in the first 37 CFR 1.78.  a) ☐ The translation of the foreign language process of the priority document is made of a claim for domest since a specific reference was included in the first sentence of the foreign language process.	nts have been received. Into have been received in Application or the documents have been received in Application (PCT Rule 17.2(a)). It of the certified copies not receive priority under 35 U.S.C. § 119 irst sentence of the specification rovisional application has been received priority under 35 U.S.C. §§ 120 irst priority under 35 U.S.C.	etion No ved in this National Stage ved. Ø(e) (to a provisional application) or in an Application Data Sheet. eceived. Ø0 and/or 121 since a specific				
Attachment(s)	A) Intention Cummo	ry (PTO-413) Paper No(s)				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)		Patent Application (PTO-152)				
3) X Information Disclosure Statement(s) (PTO-1449) Paper No(s)	. 6) ☐ Other: .					

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Serial Number: 10/631804 Attorney's Docket #: Q76384 Filing Date: 8/1/2003; claimed foreign priority to 8/2/2002

Applicant: Kubo

Examiner: Alexander Williams

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains.

Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to

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consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(f) or (g) prior art under 35 U.S.C. 103(a).

Claims 1 to 17 are rejected under 35 U.S.C. § 102(b) as being anticipated by Nakayama et al. (U.S. Patent # 6,208,023 B1).

- 1. Nakayama et al. (figures 1a to 5c) specifically figure 1b show an IC package comprising: an IC chip 11; a substrate 12 including a conductive layer 15; and a heat-radiating mechanism 12a that is mounted on the substrate, disposed between the IC chip and the substrate, and dissipates heat of the IC chip, wherein terminals (by 16b) of the IC chip and the heat-radiating mechanism are electrically connected, and the heat-radiating mechanism and the conductive layer of the substrate are electrically connected.
- 2. The IC package of claim 1, Nakayama et al. show wherein the IC chip is fixed on the heat-radiating mechanism.
- 3. The IC package of claim 1, Nakayama et al. show wherein the IC chip and the heat-radiating mechanism are electrically connected by wire bonding.
- 4. The IC package of claim 1, Nakayama et al. show wherein the IC chip and the heat-radiating mechanism are electrically connected by a conductive material.
- 5. The IC package of claim 1, Nakayama et al. further including an insulating layer 17 between the heat-radiating mechanism and the conductive layer of the substrate, wherein the heat-radiating mechanism and the conductive layer of the substrate are electrically connected via connection members disposed in plural throughholes disposed in the insulating layer.
- 6. The IC package of claim 1, Nakayama et al. show wherein the terminals of the IC chip are ground terminals and the conductive layer is a ground layer.
- 7. The IC package of claim 1, Nakayama et al. show wherein the terminals of the IC chip are power terminals and the conductive layer is a power layer.
- 8. The IC package of claim 1, Nakayama et al. show wherein the heat-radiating mechanism comprises a heat sink.
- 9. The IC package of claim 1, Nakayama et al. show wherein the heat-radiating mechanism comprises plural heat sinks, and at least part of each heat sink is disposed below the IC chip.
- 10. The IC package of claim 9, Nakayama et al. show wherein the plural heat sinks are disposed so as to be separate from each other.

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- 11. Nakayama et al. (figures 1a to 5c) specifically figure 1b show a connection structure comprising: an IC chip 11; a substrate 12 disposed with a conductive layer 15; and a heat-radiating mechanism 12a that is mounted on the substrate, disposed between the IC chip and the substrate, and dissipates heat of the IC chip, wherein terminals of the IC chip are electrically connected to the conductive layer via the heat-radiating mechanism.
- 12. The connection structure of claim 11, Nakayama et al. show wherein the IC chip and the heat-radiating mechanism are electrically connected by wire bonding.
- 13. The connection structure of claim 11, Nakayama et al. show wherein the IC chip and the heatradiating mechanism are electrically connected by a conductive material.
- 14. The connection structure of claim 11, Nakayama et al. further including an insulating layer 17 between the heat-radiating mechanism and the conductive layer of the substrate, wherein the heat-radiating mechanism and the conductive layer of the substrate are electrically connected via connection members disposed in plural throughholes disposed in the insulating layer.
- 15. Nakayama et al. (figures 1a to 5c) specifically figure 1b show a method of connecting an IC chip 11 and a substrate 12 including a conductive layer 15 sandwiched between insulating layers 17, the method comprising the steps of:
- (a) disposing a heat-radiating mechanism 12a between the IC chip and the substrate;
- (b) fixing the IC chip to the heat-radiating mechanism;
- (c) disposing plural through-holes in at least one of the insulating layers; and
- (d) disposing connection members in the through-holes so that the heat-radiating mechanism and the conductive layer of the substrate are electrically connected via the connection members.
- 16. Nakayama et al. (figures 1a to 5c) specifically figure 1b show an electrical device disposed with an IC package that includes: an IC chip 11; a substrate 12 including a conductive layer 15; and a heat-radiating mechanism 12a that is mounted on the substrate, disposed between the IC chip and the substrate, and dissipates heat of the IC chip, wherein terminals of the IC chip and the heat radiating mechanism are electrically connected, and the heat-radiating mechanism and the conductive layer of the substrate are electrically connected.
- 17. Nakayama et al. (figures 1a to 5c) specifically figure 1b show an electrical device disposed with a connection structure that includes: an IC chip 11; a substrate 12 disposed with a conductive layer 15; and a heat-radiating mechanism 12a that is mounted on the substrate, disposed between the IC chip and the substrate, and

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dissipates heat of the IC chip, wherein terminals of the IC chip are electrically connected to the conductive layer via the heat-radiating mechanism.

The listed references are cited as of interest to this application, but not applied at this time.

Field of Search	Date
U.S. Class and subclass: 257/684,796,666,698,696,675,784,786,692,693,691,712, 713,717,720	12/12/03
Other Documentation: foreign patents and literature in 257/684,796,666,698,696,675,784,786,692,693,691,712, 713,717,720	12/12/03
Electronic data base(s): U.S. Patents EAST	12/12/03

Papers related to this application may be submitted to Technology Center 2800 by facsimile transmission. Papers should be faxed to Technology Center 2800 via the Technology Center 2800 Fax center located in Crystal Plaza 4-5B15. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (November 15, 1989). The Technology Center 2800 Fax Center number is (703) 308-7722 or 24. Only Papers related to Technology Center 2800 APPLICATIONS SHOULD BE FAXED to the GROUP 2800 FAX CENTER.

Any inquiry concerning this communication or any earlier communication from the examiner should be directed to *Examiner Alexander Williams* whose telephone number is **(703) 308-4863**.

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Any inquiry of a general nature or relating to the status of this application should be directed to the *Technology Center 2800* receptionist whose telephone number is (703) 308-0956.

12/13/03

Primary Patent Examiner Alexander O. Williams